

HV Energy Storage BMS Solution Specification (52S Each Pack)

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Revision History

Date	Revised Version	Revision Note	Reviser
2025-2-22	V1.0	Newly formulate	Bin
2025-4-28	V1.1	Communication terminal optimization	Bin

Catalog

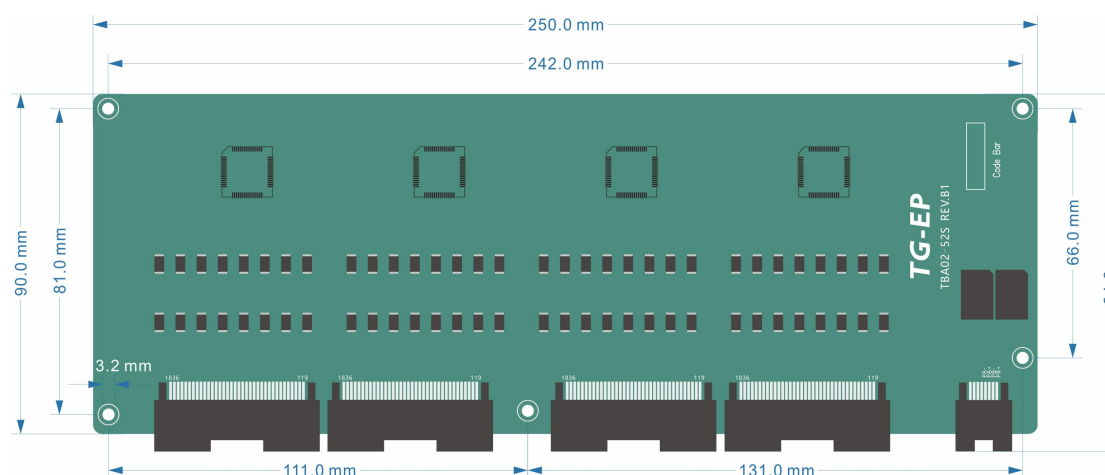
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1. Overview of the solution

This BMS solution is suitable for the 2 levels control of high voltage energy storage system, which manages the battery cluster. Configure the battery PACKs through Daisy chain communication to provide different capacity options. It can realize a variety of fault alarm and protection, and can use the upper computer for parameter setting and fault diagnosis.

2. TBA-M52S Hardware description

2.1.Main board dimension drawing



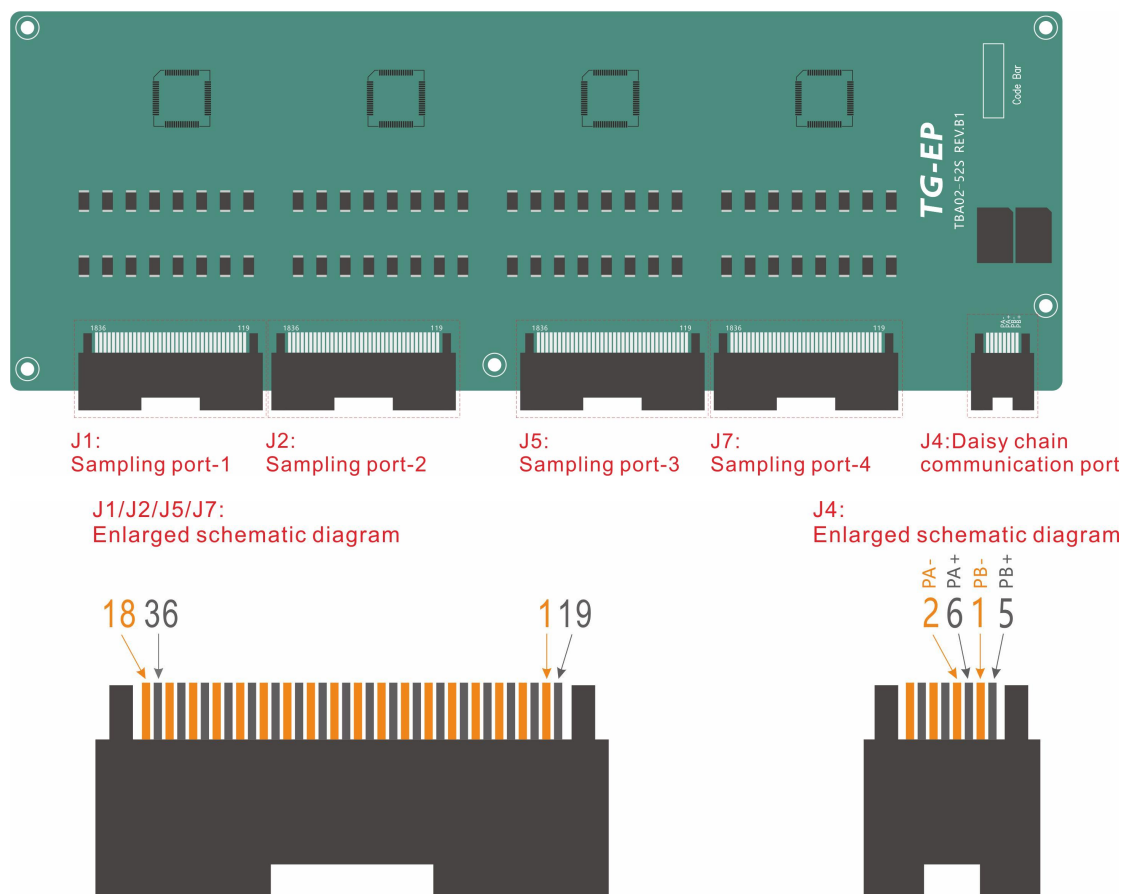
2.2. Hardware parameter list

Index item	Parameter
Overall dimensions of main board	250*90mm
Operating voltage	36V ~ 60V (AFE)
Working power consumption	Normal working power consumption $\leq 10\text{ma}$; Sleep power consumption $\leq 300\text{uA}$
Number of single cell voltage detection channels	48 road
Monomer voltage detection	0 ~ 5VDC, $\leq \pm 10\text{mV}$
Number of temperature detection channels	16 channels of battery cell temperature detection, $\leq 2^{\circ}\text{C}$, $-40^{\circ}\text{C} \sim 125^{\circ}\text{C}$

Single cell equalization mode	Passive discharge equalization, 100 ma 20%
Correspondence	Daisy chain
Working power consumption	Normal working power consumption $\leq 10\text{ma}$; Sleep power consumption $\leq 300\mu\text{A}$

2.3.Main board port function

2.3.1. Port function diagram



2.3.2. Port definition table

Port		Functional description	Remarks
J1: Sampling port			
J1-1	RT1-M1	Temp-1 detection	
J1-2	RT2-M1	Temp-2 detection	
J1-3	RT3-M1	Temp-3 detection	
J1-4	RT4-M1	Temp-4 detection	
J1-5	RT5-M1	/	

Port		Functional description	Remarks
J1-6	RT6-M1	/	
J1-7	RT7-M1	/	
J1-8	GND-M1	GND	
J1-9	1# cell B-	AFE-M1 power supply negative	
J1-10	B1-M1	1# cell positive pole	
J1-11	B3-M1	3# cell positive pole	
J1-12	B5-M1	5# cell positive pole	
J1-13	B7-M1	7# cell positive pole	
J1-14	B9-M1	9# cell positive pole	
J1-15	B11-M1	11# cell positive pole	
J1-16	B13-M1	13# cell positive pole	
J1-17	/	/	
J1-18	12# cell B+	AFE-M1 power supply positive	
J1-19	GND	GND	
J1-20	GND		
J1-21	GND		
J1-22	GND		
J1-23	GND		
J1-24	GND		
J1-25	GND		
J1-26	GND		
J1-27	GND		
J1-28	B0-M1	Battery negative(1# cell negative pole)	
J1-29	B2-M1	2# cell positive pole	
J1-30	B4-M1	4# cell positive pole	
J1-31	B6-M1	6# cell positive pole	
J1-32	B8-M1	8# cell positive pole	
J1-33	B10-M1	10# cell positive pole	

Port		Functional description	Remarks
J1-34	B12-M1	12# cell positive pole	
J1-35	/	/	
J1-36	/	/	
J2: Sampling port			
J2-1	RT1-M2	Temp-5 detection	
J2-2	RT2-M2	Temp-6 detection	
J2-3	RT3-M2	Temp-7 detection	
J2-4	RT4-M2	Temp-8 detection	
J2-5	RT5-M2	/	
J2-6	RT6-M2	/	
J2-7	RT7-M2	/	
J2-8	GND-M2	GND	
J2-9	13# cell B-	AFE-M2 power supply negative	
J2-10	B1-M2	14# cell positive pole	
J2-11	B3-M2	16# cell positive pole	
J2-12	B5-M2	18# cell positive pole	
J2-13	B7-M2	20# cell positive pole	
J2-14	B9-M2	22# cell positive pole	
J2-15	B11-M2	24# cell positive pole	
J2-16	B13-M2	26# cell positive pole	
J2-17	/	/	
J2-18	24# cell B+	AFE-M2 power supply positive	
J2-19	GND	GND	
J2-20	GND		
J2-21	GND		
J2-22	GND		
J2-23	GND		
J2-24	GND		

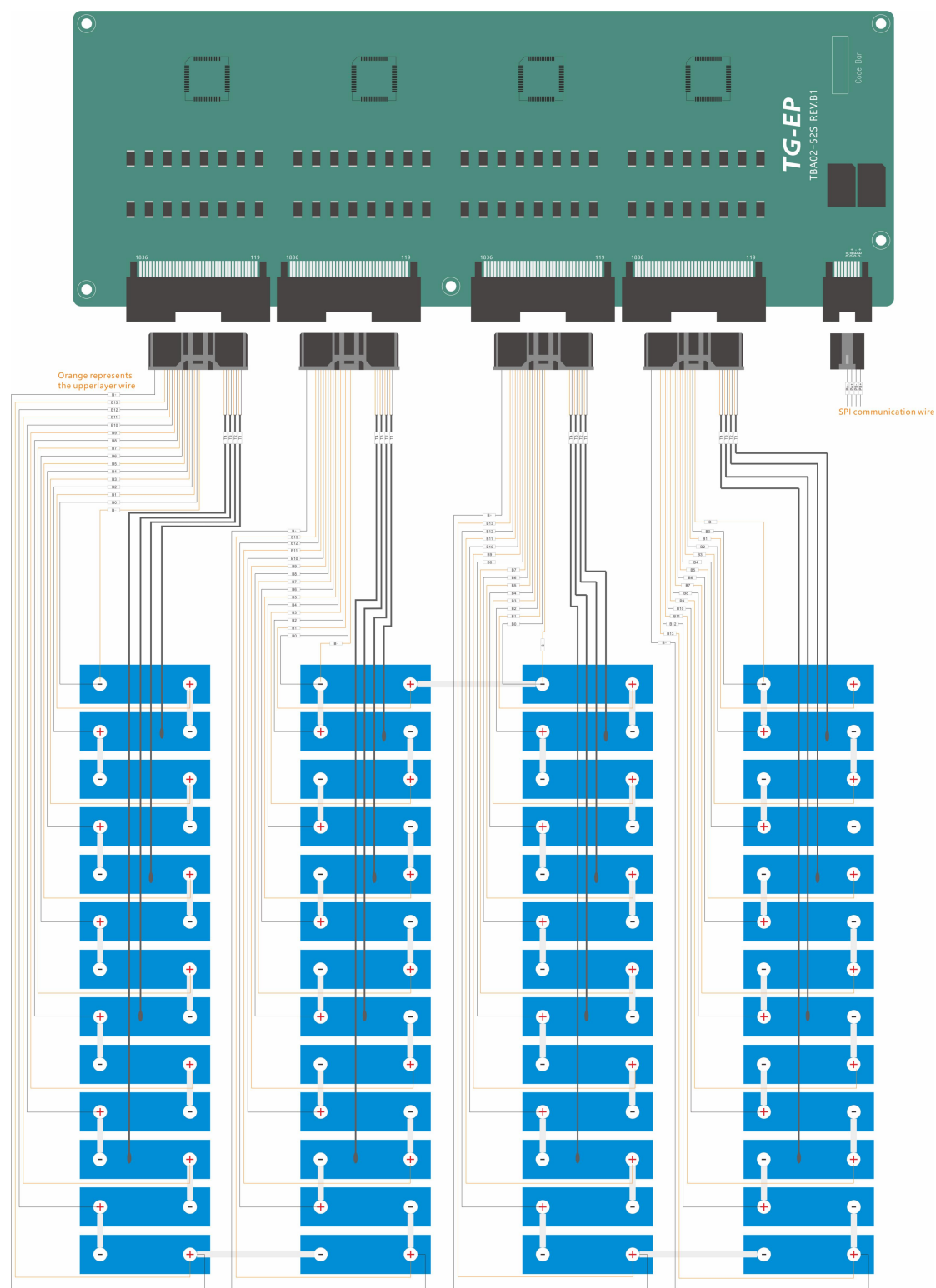
Port		Functional description	Remarks
J2-25	GND		
J2-26	GND		
J2-27	GND		
J2-28	B0-M2	Battery negative(13# cell negative pole)	
J2-29	B2-M2	15# cell positive pole	
J2-30	B4-M2	17# cell positive pole	
J2-31	B6-M2	19# cell positive pole	
J2-32	B8-M2	21# cell positive pole	
J2-33	B10-M2	23# cell positive pole	
J2-34	B12-M2	25# cell positive pole	
J2-35	/	/	
J2-36	/	/	
J5: Sampling port			
J5-1	RT1-M3	Temp-9 detection	
J5-2	RT2-M3	Temp-10 detection	
J5-3	RT3-M3	Temp-11 detection	
J5-4	RT4-M3	Temp-12 detection	
J5-5	RT5-M3	/	
J5-6	RT6-M3	/	
J5-7	RT7-M3	/	
J5-8	GND-M3	GND	
J5-9	25# cell B-	AFE-M3 power supply negative	
J5-10	B1-M3	27# cell positive pole	
J5-11	B3-M3	29# cell positive pole	
J5-12	B5-M3	31# cell positive pole	
J5-13	B7-M3	33# cell positive pole	
J5-14	B9-M3	35# cell positive pole	

Port		Functional description	Remarks
J5-15	B11-M3	37# cell positive pole	
J5-16	B13-M3	39# cell positive pole	
J5-17	/	/	
J5-18	36# cell B+	AFE-M3 power supply positive	
J5-19	GND	GND	
J5-20	GND		
J5-21	GND		
J5-22	GND		
J5-23	GND		
J5-24	GND		
J5-25	GND		
J5-26	GND		
J5-27	GND		
J5-28	B0-M3	Battery negative(25# cell negative pole)	
J5-29	B2-M3	28# cell positive pole	
J5-30	B4-M3	30# cell positive pole	
J5-31	B6-M3	32# cell positive pole	
J5-32	B8-M3	34# cell positive pole	
J5-33	B10-M3	36# cell positive pole	
J5-34	B12-M3	38# cell positive pole	
J5-35	/	/	
J5-36	/	/	
J7: Sampling port			
J7-1	RT1-M4	Temp-13 detection	
J7-2	RT2-M4	Temp-14 detection	
J7-3	RT3-M4	Temp-15 detection	
J7-4	RT4-M4	Temp-16 detection	

Port		Functional description	Remarks
J7-5	RT5-M4	/	
J7-6	RT6-M4	/	
J7-7	RT7-M4	/	
J7-8	GND-M4	GND	
J7-9	37# cell B-	AFE-M4 power supply negative	
J7-10	B1-M4	40# cell positive pole	
J7-11	B3-M4	42# cell positive pole	
J7-12	B5-M4	44# cell positive pole	
J7-13	B7-M4	46# cell positive pole	
J7-14	B9-M4	48# cell positive pole	
J7-15	B11-M4	50# cell positive pole	
J7-16	B13-M4	52# cell positive pole	
J7-17	/	/	
J7-18	48# cell B+	AFE-M4 power supply positive	
J7-19	GND	GND	
J7-20	GND		
J7-21	GND		
J7-22	GND		
J7-23	GND		
J7-24	GND		
J7-25	GND		
J7-26	GND		
J7-27	GND		
J7-28	B0-M4	Battery negative(37# cell negative pole)	
J7-29	B2-M4	41# cell positive pole	
J7-30	B4-M4	43# cell positive pole	
J7-31	B6-M4	45# cell positive pole	
J7-32	B8-M4	47# cell positive pole	

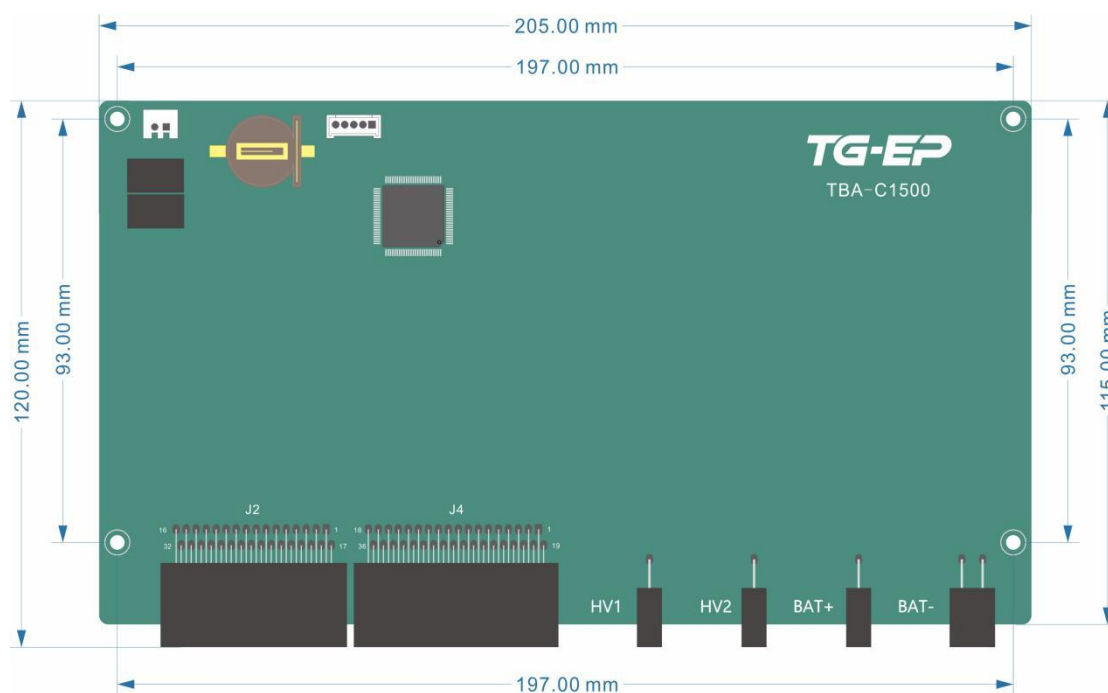
Port		Functional description	Remarks
J7-33	B10-M4	49# cell positive pole	
J7-34	B12-M4	51# cell positive pole	
J7-35	/	/	
J7-36	/	/	
J4 Communication Port			
J3-1	PB-		
J3-2	PA-		
J3-3	/		
J3-4	/		
J3-5	PB+		
J3-6	PA+		
J3-7	/		
J3-8	/		

2.4. Sampling Wiring Diagram



3. TBA-C1500 Hardware description

3.1.Main board dimension drawing

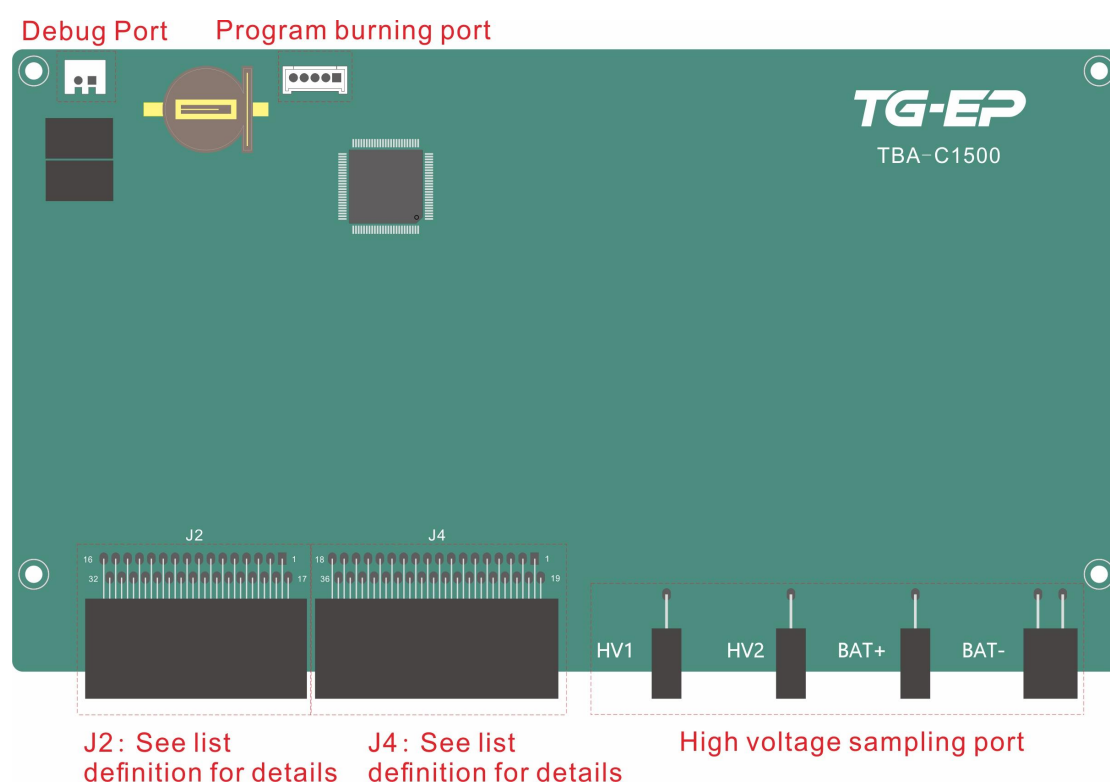


3.2. Hardware parameter list

Index item	Parameter
Overall dimensions of main board	205*115mm
Operating voltage	9 ~ 30 VDC (matching the working power supply range of hardware)
SOC accuracy	≤ 5%, according to YD/T2344.1-2011.
Data storage	≥ 10,000 historical data records
Pre-charging function	Have
Charging current limiting	Have
Upper computer	Have
Program upgrade	Have
Communication methods	CAN, RS485, WIFI (extended), 4G (extended)

3.3.Main board port function

3.3.1. Port function diagram

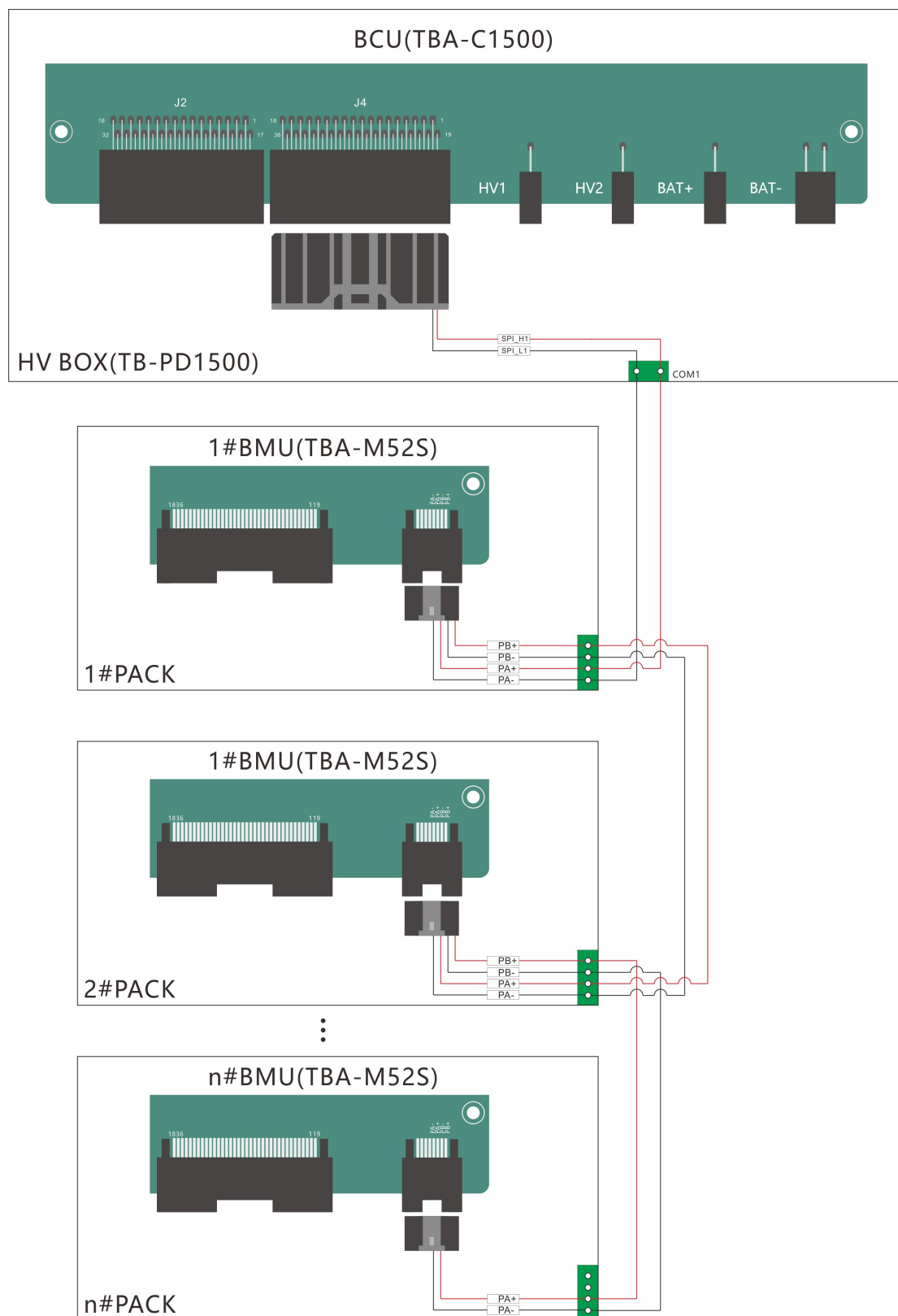


3.3.2. Port definition table

Post	Bit number	Definition	Bit number	Definition
J2	1	/	17	PE
	2	HALL_I2	18	HALL_I1
	3	HALL_5V+	19	HALL_5V-
	4	DIH1	20	GND
	5	DIH2	21	SW1_OUTA
	6	DIH3	22	SW1_OUTB
	7	WKUP	23	SW2_OUTA
	8	DOL1	24	SW2_OUTB
	9	DOL2	25	SW3_OPA
	10	DOL3	26	SW3_OPB
	11	DOH1	27	GND
	12	DOH2	28	GND

	13	DOH3	29	GND
	14	DOH4	30	GND
	15	PW+	31	PW+
	16	PW-	32	PW-
J4	1	SPI_L1	19	SPI_H1
	2	SPI_L2	20	SPI_H2
	3	CAN1H	21	CAN1R
	4	GND_CAN1	22	CAN1L
	5	CAN2H	23	CAN2R
	6	GND_CAN2	24	CAN2L
	7	CAN0L	25	CAN0H
	8	485A1	26	GND_CAN0
	9	GND_4851	27	485B1
	10	485B2	28	485A2
	11	DIL1	29	GND_4852
	12	DIL3	30	DIL2
	13	GND	31	DO_ADR
	14	SW1_IN-	32	SW1_IN+
	15	SW2_IN-	33	SW2_IN+
	16	TEMP2	34	TEMP1
	17	TEMP4	35	TEMP3
	18	TGND	36	TGND
HV1	1	High voltage sampling 1	2	/
HV2	1	High voltage sampling 2	2	/
BAT+	1	High voltage sampling positive	2	/
NE	1	Shunt input negative (IS-)	2	High voltage sampling negative (BAT-)
	3	Shunt input positive (IS+)	4	/

3.4. Master-slave Communication Wiring Diagram



4. Display Installation Instructions

4.1. Display Appearance (7 inch)



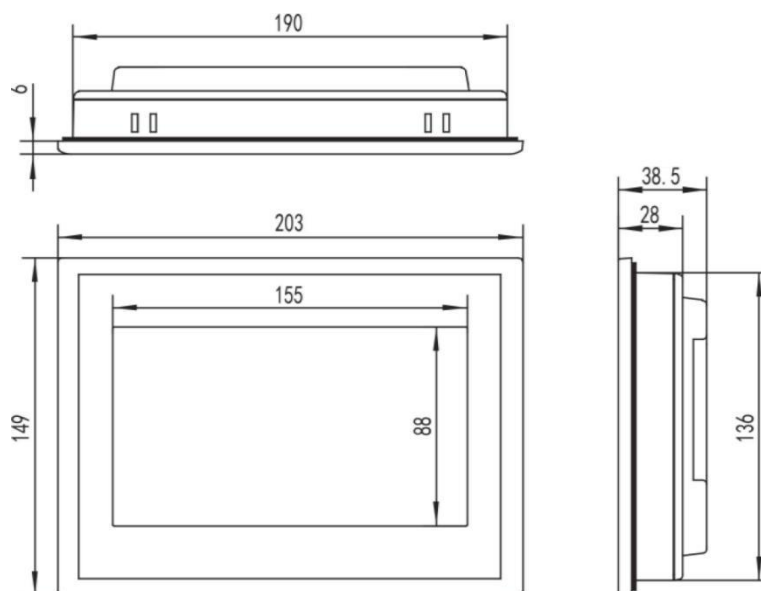
4.1.1. Product Specifications

Category	Details	Parameter
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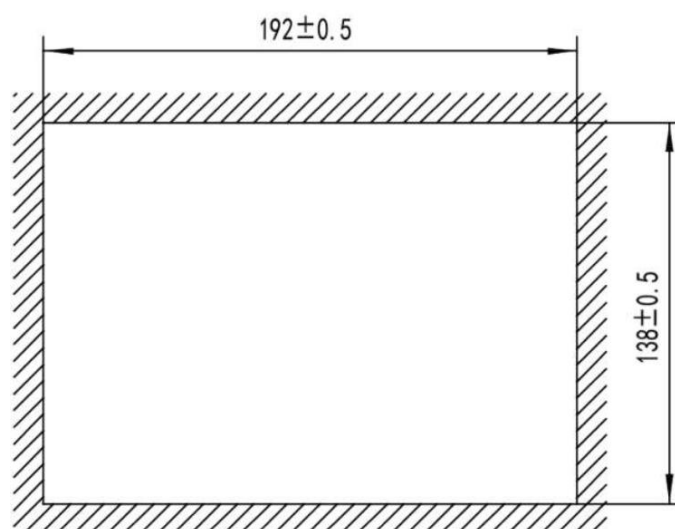
Product feature	LCD Screen	7"TFT
	Backlight Type	LED
	Resolution	800 x480
	Display Brightness	250cd/m
	Touch screen	4-Wire Resistive
	Input Voltage	24±20%VDC
	Rated Power	5W
	Processor	ARM 600MHz
	Memory	64MB
	System Storage	128MB
	Storage Space	8M
	Hardware Clock	Built-in
Environmental Conditions	Operating Temperature	0°C~45°C
	Operating Humidity	5%~90%(Non-condensing)
	Storage Temperature	-10°C~60°C
	Storage Humidity	5%~90%Non-condensing)
Product Specifications	Enclosure Color	Black
	External Dimensions	203*149mm
	Cutout Dimensions	192*138mm
	Display Dimensions	155*88mm

4.1.2. Product Dimensions

External Dimensions:



Cutout Dimensions:

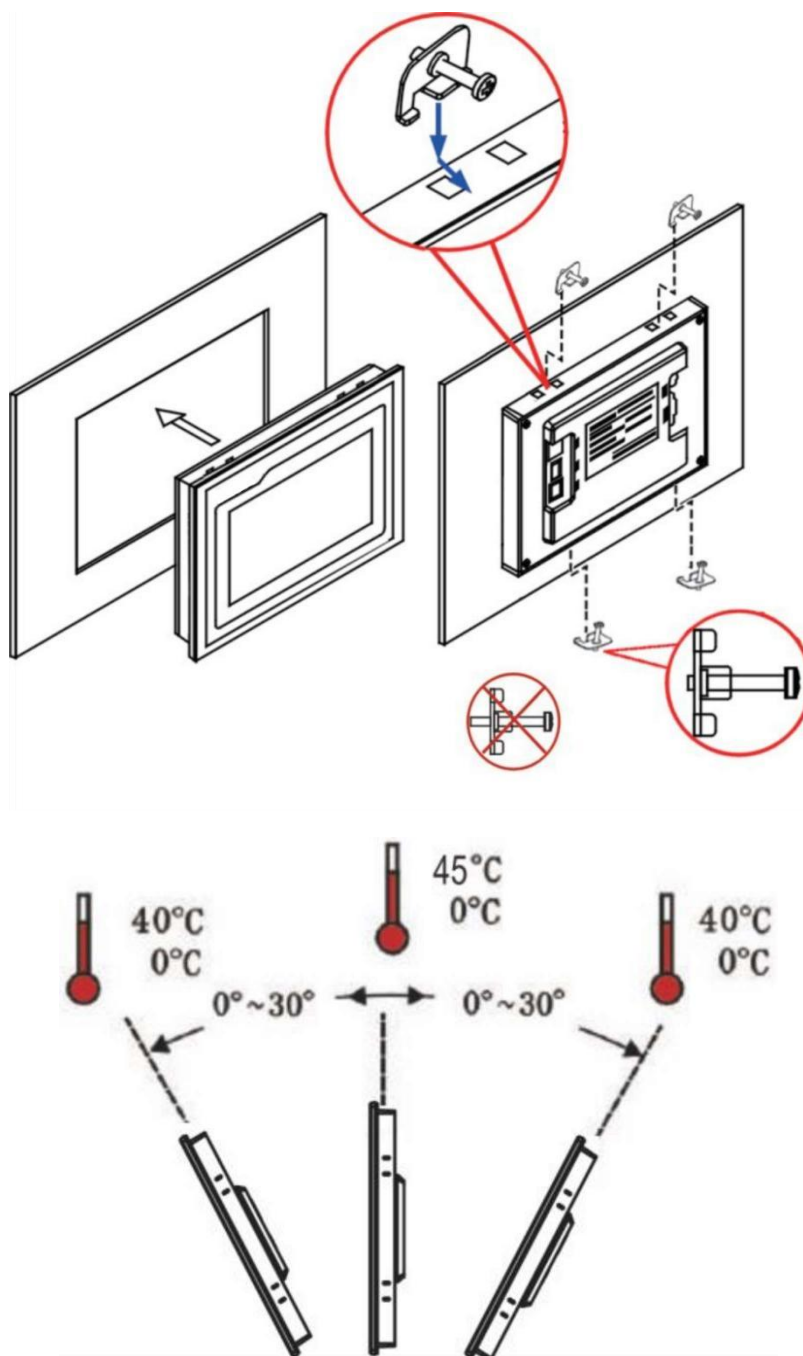


4.1.3. Port Definitions



Port	Name	Definition	Function
①	Power	DC24V/0V	Power supply for the touch screen
②	Communication	RS485+/RS485-	Communication with BMS
③	USB	Firmware upgrade; Data export	

4.1.4. Installation Diagram



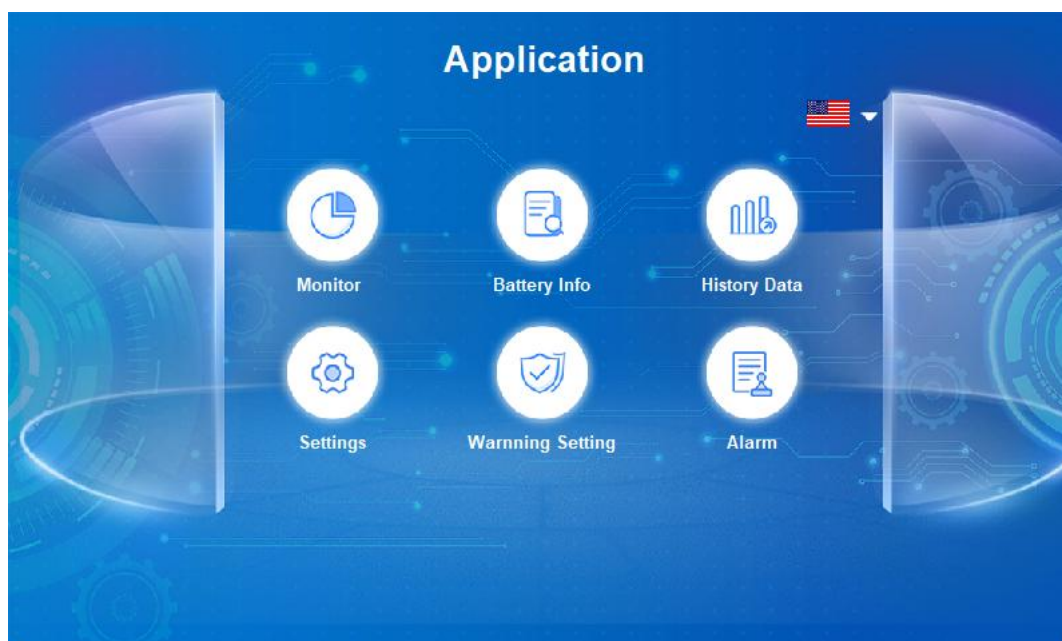
Installation Angle

5. Display Operation Instructions

5.1.Application Home Page

As shown below, the application home page mainly includes six functional entries:

"Status Monitoring", "Battery Information", "Historical Data", "Parameter Settings", "Alarm Settings", and "Fault Information". Additionally, the language can be switched between Chinese and English on this page.



5.2. Status Monitoring

As shown below, The status monitoring page displays the following information:

Power curve and key data of Battery Pack.



5.3. Battery Information

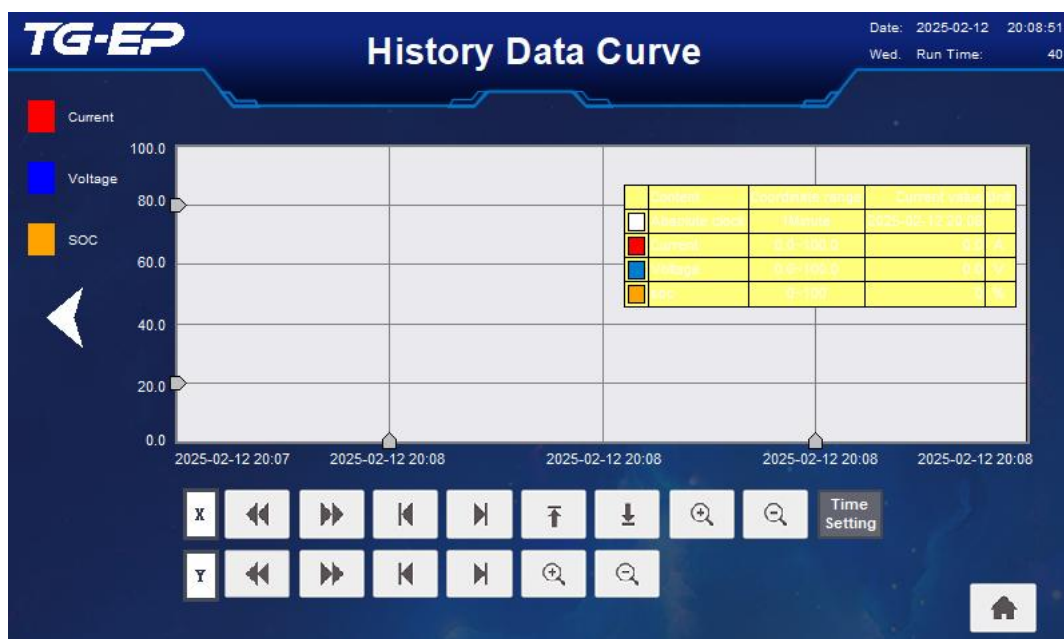
As show below, the battery information page provides detailed data for both the battery pack and battery modules.



5.4. Historical Data

As shown below, The historical data page allows users to query historical data for "Current", "Voltage" and "SOC" . Additionally, historical data trends can be analyzed by

viewing historical data curves.



5.5. Parameter settings

As shown below, The parameter settings page allows configuration of all adjustable parameters. For a complete list of parameters, refer to the TBA-C1500 Specification Document.

TG-EP Settings Date: 2025-02-12 19:04:21 Wed. Run Time: 16

485-1	485-2	485-3
Baudrate: 4800	Baudrate: 4800	Baudrate: 4800
Checksum: None	Checksum: None	Checksum: None
Addr: 0	Addr: 0	Addr: 0
Protocol: 1	Protocol: 1	Protocol: 1

Temp Control	System Parar
Heating Open Temp(°C): 0.00	BMU Number: 0
Heating Close Temp(°C): 0.00	Cells Number: 0
Cooler Open Temp(°C): 0.00	Temp Number: 0
Cooler Close Temp(°C): 0.00	Standby Sleep Time(h): 0
	Rated Capacity(Ah): 0
	Cycle Count: 0
	SOC Setting(%): 0
	SOE Setting(%): 0

Navigation: [Back] 1 / 8 [Forward] [Home]

5.6. Alarm Settings

As shown below, The alarm settings page allows configuration of all alarm-related parameters. For a complete list of alarm parameters, refer to the TBA-C1500 Specification Document.

TG-EP Warning Setting Date: 2025-02-12 19:27:20 Wed. Run Time: 58

Discharge Total Voltage UV(High Temp) Alarm					
Warning Value: 0.0 V	Release Value: 0.0 V	Current Ratio: 0 %			
Warning Delay: 0.00 Sec	Release Delay: 0.00 Sec	Handle: Disable			

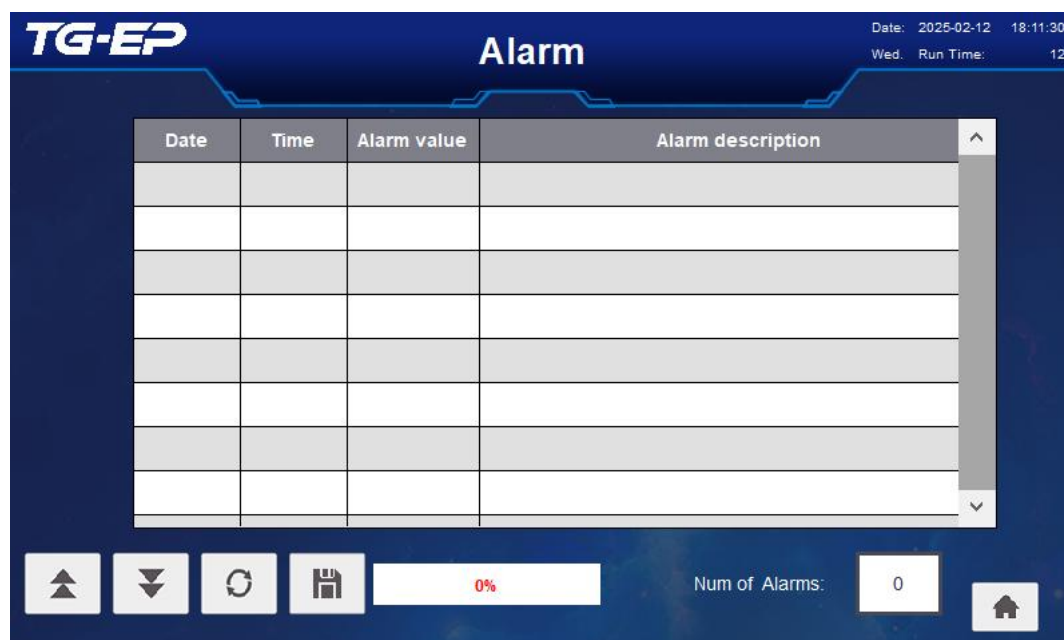
Discharge Total Voltage UV(High Temp) Protect					
Warning Value: 0.0 V	Release Value: 0.0 V	Current Ratio: 0 %			
Warning Delay: 0.00 Sec	Release Delay: 0.00 Sec	Handle: Disable			

Discharge Total Voltage UV(High Temp) Serious Fault					
Warning Value: 0.0 V	Release Value: 0.0 V	Current Ratio: 0 %			
Warning Delay: 0.00 Sec	Release Delay: 0.00 Sec	Handle: Disable			

Navigation: [Back] 1 / 20 [Forward] [Home]

5.7. Fault Query

As shown below, The fault query page displays all recorded fault information.



6. BCU Parameter list

In battery faults, Level 1 indicates a minor fault, Level 2 indicates a moderate fault, and Level 3 indicates a severe fault.

Alarm: When a fault is triggered, the alarm status is reported. When the release conditions are met, the alarm status is cleared.

Relay Disconnection: When a fault is triggered, the alarm status is reported, and the relay is disconnected. When the release conditions are met, the alarm status is cleared, and the relay is reconnected.

Restart Release: When a fault is triggered, the alarm status is reported, and the relay is disconnected. When the release conditions are met, the alarm status is not cleared, and the relay remains disconnected until a power cycle is performed.

6.1.Alarm Trigger

1. During charging, monitor charging alarms and general alarms.

Trigger the charging or general alarm :

the current battery parameter value $\geq$ fault value and the duration $\geq$ delay time.

2. During discharging, monitor discharging alarms and general alarms.

Trigger the discharging or general alarm :

the current battery parameter value $\geq$ fault value and the duration $\geq$ delay time.

3. During standby, monitor general, charging, and discharging alarms.

Trigger the charging/discharging or general alarm:

the current battery parameter value $\geq$ fault value and the duration $\geq$ delay time.

6.2. Alarm Release

1. The fault is released (including charging, discharging, and general alarms) if the current battery parameter value $<$ release value and the duration $\geq$ delay time.

2. During discharging, charging alarms are cleared.

3. During charging, discharging alarms are cleared.

6.3. Charge/Discharge Alarm Parameter

Fault Name	Fault Level	Fault Value	Delay	Release Value	Delay	Reduce Current	Trigger
Discharge Total Voltage Under-voltage	1 level	2.90*Total Cells	3S	3.10*Total Cells	3S	No reduction	Alarm
	2 level	2.80*Total Cells	3S	3.00*Total Cells	3S	Reduce current to 0	Alarm
	3 level	2.70*Total Cells	3S	2.90*Total Cells	3S	Reduce current to 0	Relay disconnection
Discharge Cell Under-voltage	1 level	2.90V	3S	3.10V	3S	No reduction	Alarm
	2 level	2.80V	3S	3.00V	3S	Reduce current to 0	Alarm
	3 level	2.70V	3S	2.90V	3S	Reduce current to 0	Relay disconnection
Discharge Over-current	1 level	Rated current A	3S	Rated current-10A	3S	No reduction	Alarm

Fault Name	Fault Level	Fault Value	Delay	Release Value	Delay	Reduce Current	Trigger
	2 level	Rated current+20A	3S	Rated current	3S	Reduce current to 0	Alarm
	3 level	Rated current+50A	3S	Rated current+20A	3S	Reduce current to 0	Relay disconnection
Discharge High Temperature	1 level	50°C	3S	45°C	3S	No reduction	Alarm
	2 level	55°C	3S	50°C	3S	Reduce current to 0	Alarm
	3 level	60°C	3S	55°C	3S	Reduce current to 0	Relay disconnection
Discharge Low Temperature	1 level	-5°C	3S	0°C	3S	No reduction	Alarm
	2 level	-10°C	3S	-5°C	3S	Reduce current to 0	Alarm
	3 level	-20°C	3S	-10°C	3S	Reduce current to 0	Relay disconnection
Discharge Voltage Difference	1 level	0.4V	3S	0.35V	3S	No reduction	Alarm
	2 level	0.6V	3S	0.55V	3S	Reduce current to 0	Alarm
	3 level	1.0V	3S	0.95V	3S	Reduce current to 0	Relay disconnection
Discharge Temperature Difference	1 level	10°C	3S	7°C	3S	No reduction	Alarm
	2 level	13°C	3S	10°C	3S	Reduce current to 0	Alarm
	3 level	15°C	3S	12°C	3S	Reduce current to 0	Relay disconnection
Low SOC During Discharge	1 level	15%	3S	17%	3S	No reduction	Alarm
Charge Total Voltage Under-voltage	1 level	3.55*Total Cells	3S	3.40*Total Cells	3S	No reduction	Alarm
	2 level	3.60*Total Cells	3S	3.45*Total Cells	3S	Reduce current to 0	Alarm
	3 level	3.65*Total Cells	3S	3.55*Total Cells	3S	Reduce current to 0	Relay disconnection
Charge Cell Under-voltage	1 level	3.55V	3S	3.40V	3S	No reduction	Alarm
	2 level	3.60V	3S	3.45V	3S	Reduce current to 0	Alarm
	3 level	3.65V	3S	3.55V	3S	Reduce current to 0	Relay disconnection

Fault Name	Fault Level	Fault Value	Delay	Release Value	Delay	Reduce Current	Trigger
Charge Over-current	1 level	Rated current+0 0A	3S	Rated current-20 A	3S	No reduction	Alarm
	2 level	Rated current+2 0A	3S	Rated current	3S	Reduce current to 0	Alarm
	3 level	Rated current+5 0A	3S	Rated current+20 A	3S	Reduce current to 0	Relay disconnection
Charge High Temperature	1 level	45°C	3S	40°C	3S	No reduction	Alarm
	2 level	50°C	3S	45°C	3S	Reduce current to 0	Alarm
	3 level	55°C	3S	50°C	3S	Reduce current to 0	Relay disconnection
Charge Low Temperature	1 level	5°C	3S	10°C	3S	No reduction	Alarm
	2 level	0°C	3S	5°C	3S	Reduce current to 0	Alarm
	3 level	-5°C	3S	0°C	3S	Reduce current to 0	Relay disconnection
Charge Voltage Difference	1 level	0.4V	3S	0.35V	3S	No reduction	Alarm
	2 level	0.6V	3S	0.55V	3S	Reduce current to 0	Alarm
	3 level	1.0V	3S	0.95V	3S	Reduce current to 0	Relay disconnection
Charge Temperature Difference	1 level	10°C	3S	7°C	3S	No reduction	Alarm
	2 level	13°C	3S	10°C	3S	Reduce current to 0	Alarm
	3 level	15°C	3S	12°C	3S	Reduce current to 0	Relay disconnection

6.4. General Alarm Parameter

Fault Name	Fault Level	Fault Value	Delay	Release Value	Delay	Reduce Current	Trigger
Insulation Fault	1 level	500Ω/V	3S	600Ω/V	3S	No reduction	Alarm
	2 level	350Ω/V	3S	450Ω/V	3S	Reduce current to 0	Alarm
	3 level	200Ω/V	3S	300Ω/V	3S	Reduce current to 0	Relay disconnection
T1 High	/	60°C	3S	50°C	3S	Reduce	Relay

Fault Name	Fault Level	Fault Value	Delay	Release Value	Delay	Reduce Current	Trigger
Temperature						current to 0	disconnection
T1 Low Temperature	/	-5°C	3S	-2°C	3S	Reduce current to 0	Relay disconnection
T2 High Temperature	/	60°C	3S	50°C	3S	Reduce current to 0	Relay disconnection
T2 Low Temperature	/	-5°C	3S	-2°C	3S	Reduce current to 0	Relay disconnection
High Voltage Anomaly		30V	15S	10V	3S	Reduce current to 0	Restart to release
Master-Slave Communication Fault	/	Mismatch between configured and online slave devices.	20S	Match between configured and online slave devices.	5S	Reduce current to 0	Relay disconnection
Standby Sleep	Standby Sleep Delay			1440min			
	Standby Sleep Current			2A			
Balance	Turn-on voltage			3450mV			Turn-on after 1 hour of inactivity
	Turn-on Voltage Difference			40mV			
	Turn-off Voltage Difference			20mV			
Full Charge Calibration	1. Maximum cell voltage $\geq$ Full charge calibration voltage (3650mV, configurable) AND Average cell voltage $\geq$ Full charge calibration average voltage (3650mV, configurable) 2. Total battery voltage $\geq$ Full charge calibration total voltage (3650mV * Number of cells, configurable)						If either condition is met for 1s, SOC is calibrated to 100%
Full Discharge Calibration	1. Minimum cell voltage $\leq$ 2700mV (configurable) And Average cell voltage $\leq$ 2700mV (configurable) 2. Total battery voltage $\leq$ Full discharge calibration total voltage (2700mV * Number of cells, configurable)						If either condition is met for 1s, SOC is calibrated to 0%

7. Other precautions

Before installation or operation, please carefully read this manual and precautions to prevent accidents. Ensure compliance with industry safety standards and engineering design specifications during installation and operation. Adhere to the precautions and safety instructions provided by Shenzhen Tringo Control Co., Ltd.

- The external switch on the circuit board is forbidden to connect with other equipment. If necessary, please connect with the technology engineer for confirmation, otherwise we will not bear any responsibility for damaging the circuit board.
- When assembling, the BMS should not directly touch the surface of the battery cell to avoid damaging the battery cell; The assembly should be firm and reliable.
- Be careful not to touch the components on the circuit board, such as lead head, soldering iron and solder, otherwise it may damage the circuit board.
- During use, attention should be paid to prevent static electricity, moisture, and water damage.
- Please follow the design parameters and use conditions during use, and do not exceed the values in this specification, otherwise the BMS may be damaged.
- Power on and off, you must operate in the order of power on and off.
- When connecting the battery pack, do not connect it wrongly or even reversely.
- After assembling the battery pack and the BMS, if no voltage output or charging is found during the initial power-on, please check whether the wiring is correct.
- The final interpretation right belongs to our company.

Shenzhen Tringo Control Co., Ltd.

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Our company reserves the right to modify the equipment parameters without prior notice.